

FIG. 1 100

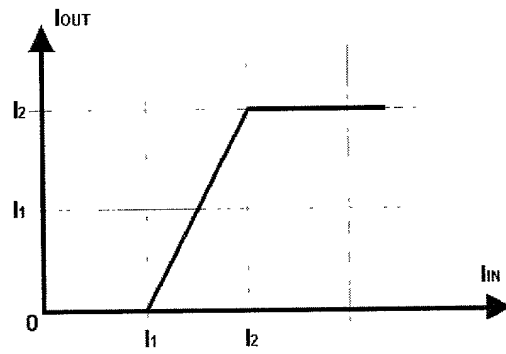


FIG. 2A 200

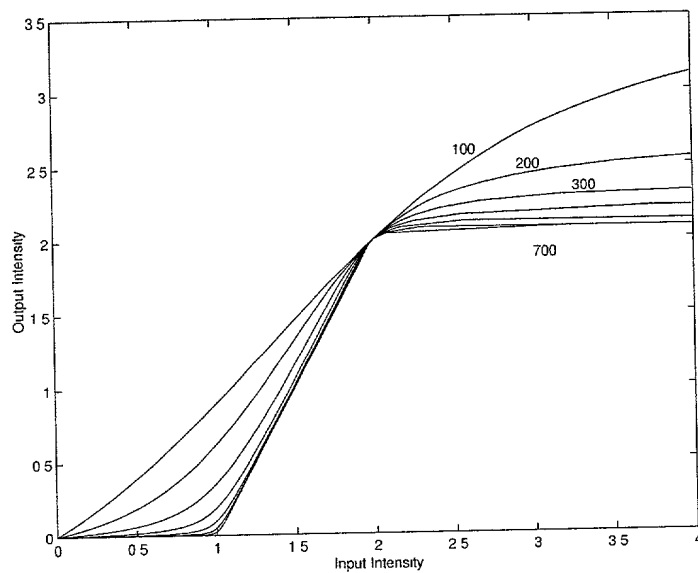


FIG. 2B 210

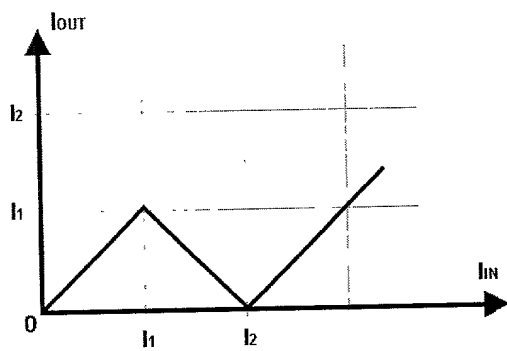


FIG. 3 300

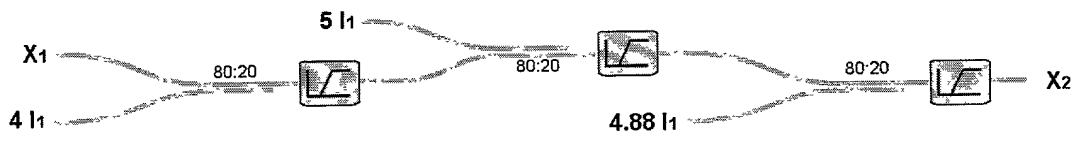


FIG. 4 400

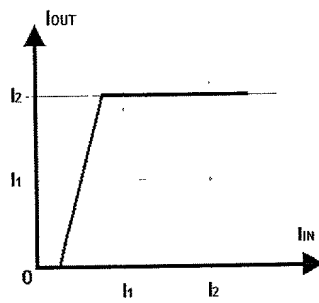
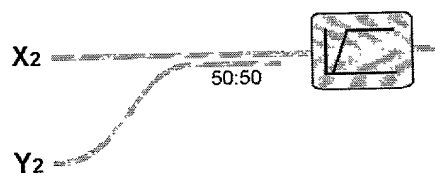
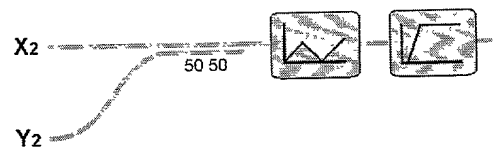
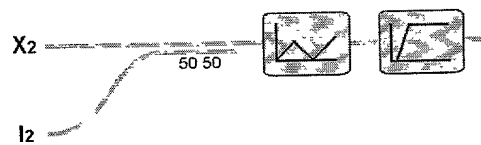


FIG. 5 500

THE UNIVERSITY OF CHICAGO

FIG. 6 600FIG. 7 700FIG. 8 800FIG. 9 900

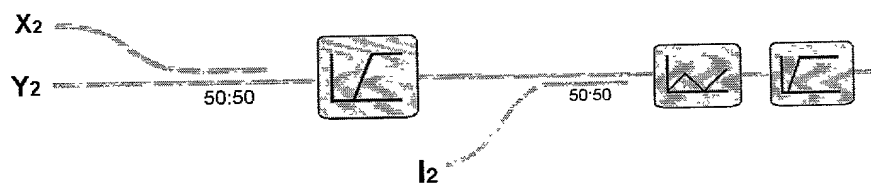


FIG. 10 1000

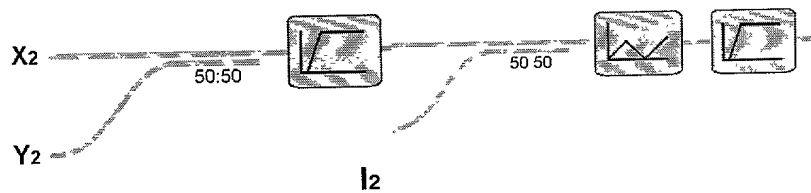


FIG. 11 1100

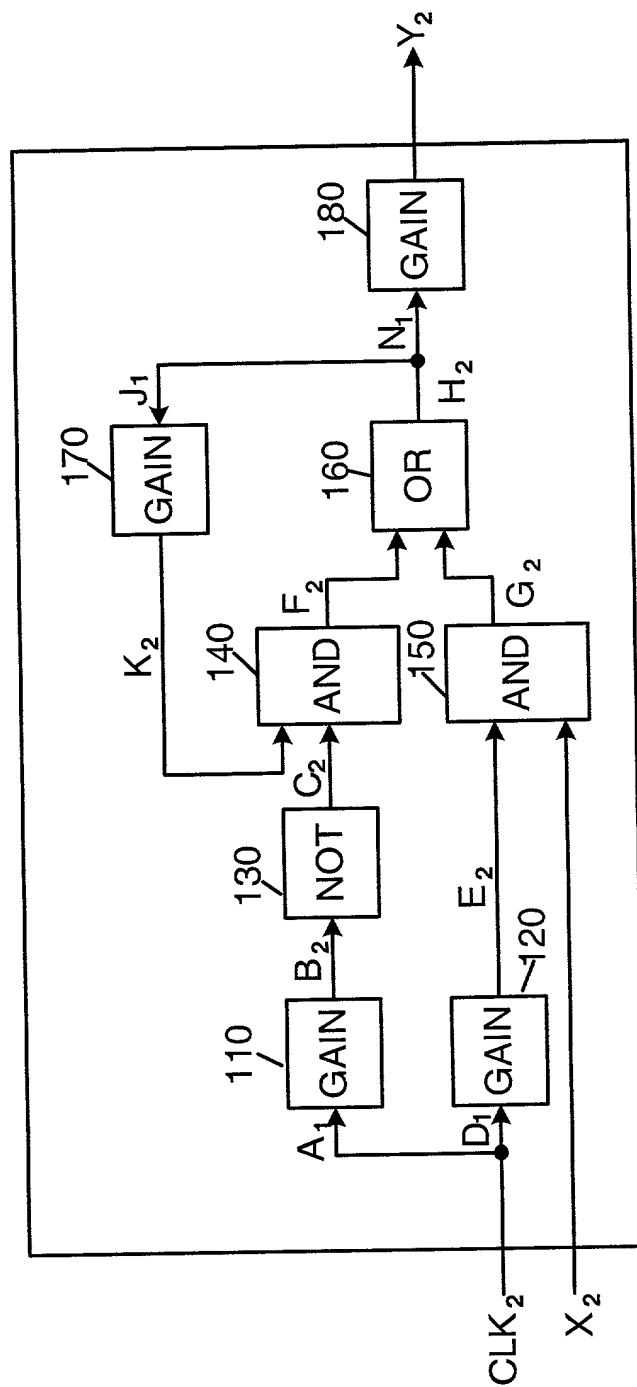


FIG. 12 1200

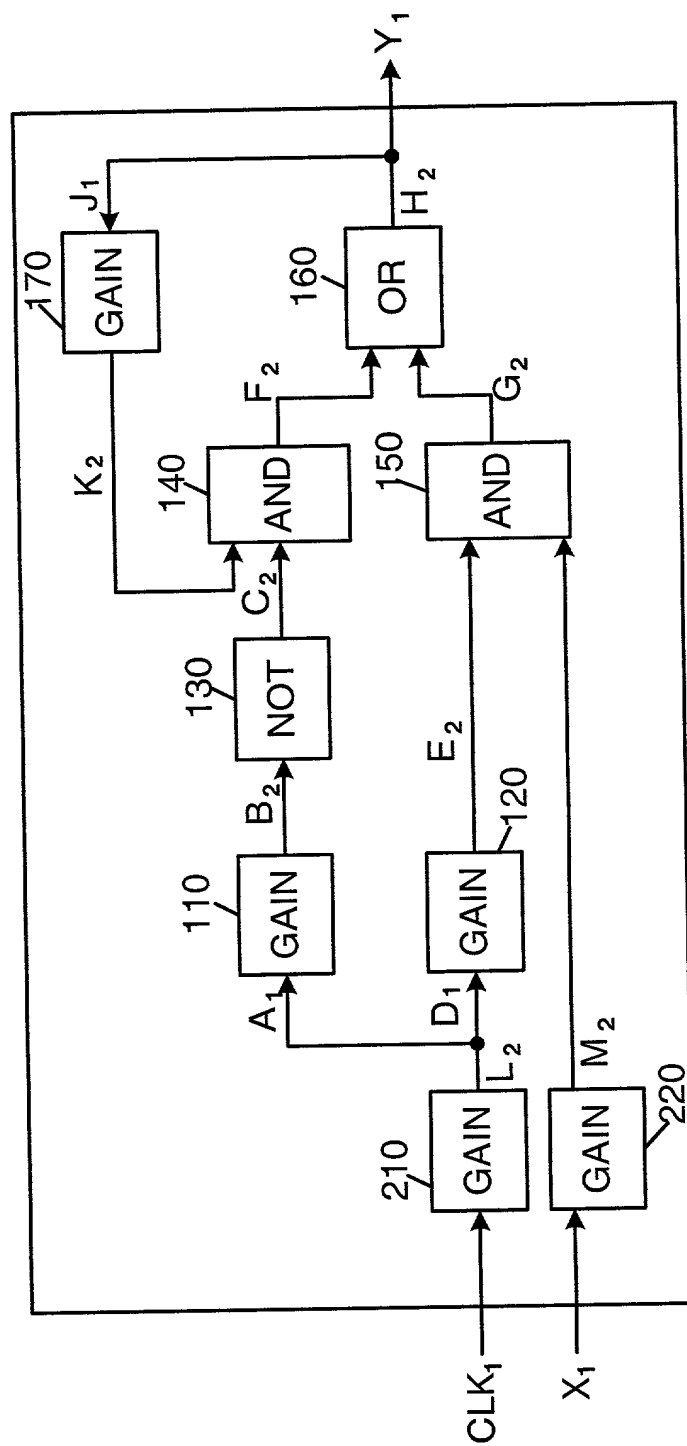


FIG. 13 1300

	$0 < \text{INPUT} < I1$	$I1 < \text{INPUT} < I2$	$I2 < \text{INPUT}$
ITRANSMITTED	0	$2 * \text{INPUT} - I2$	$I2$
IREFLECTED	INPUT	$I2 - \text{INPUT}$	$\text{INPUT} - I2$

FIG. 14 1400

20250201 24:49:01

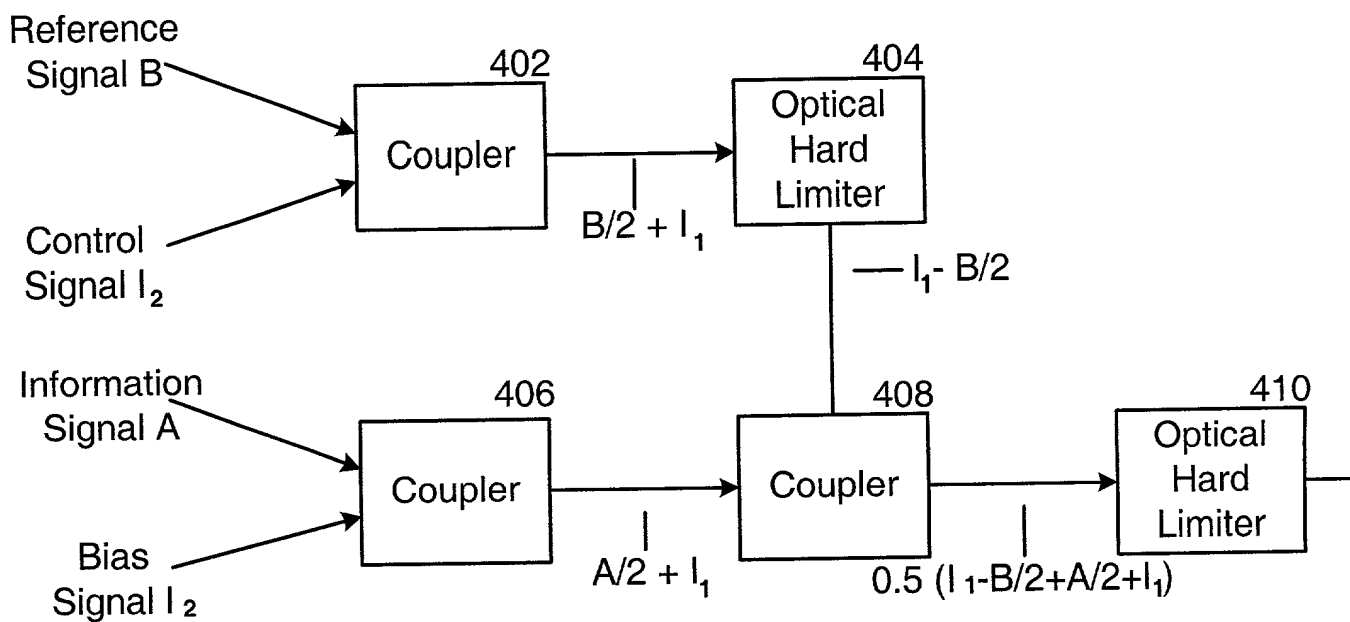


FIG. 15 1500

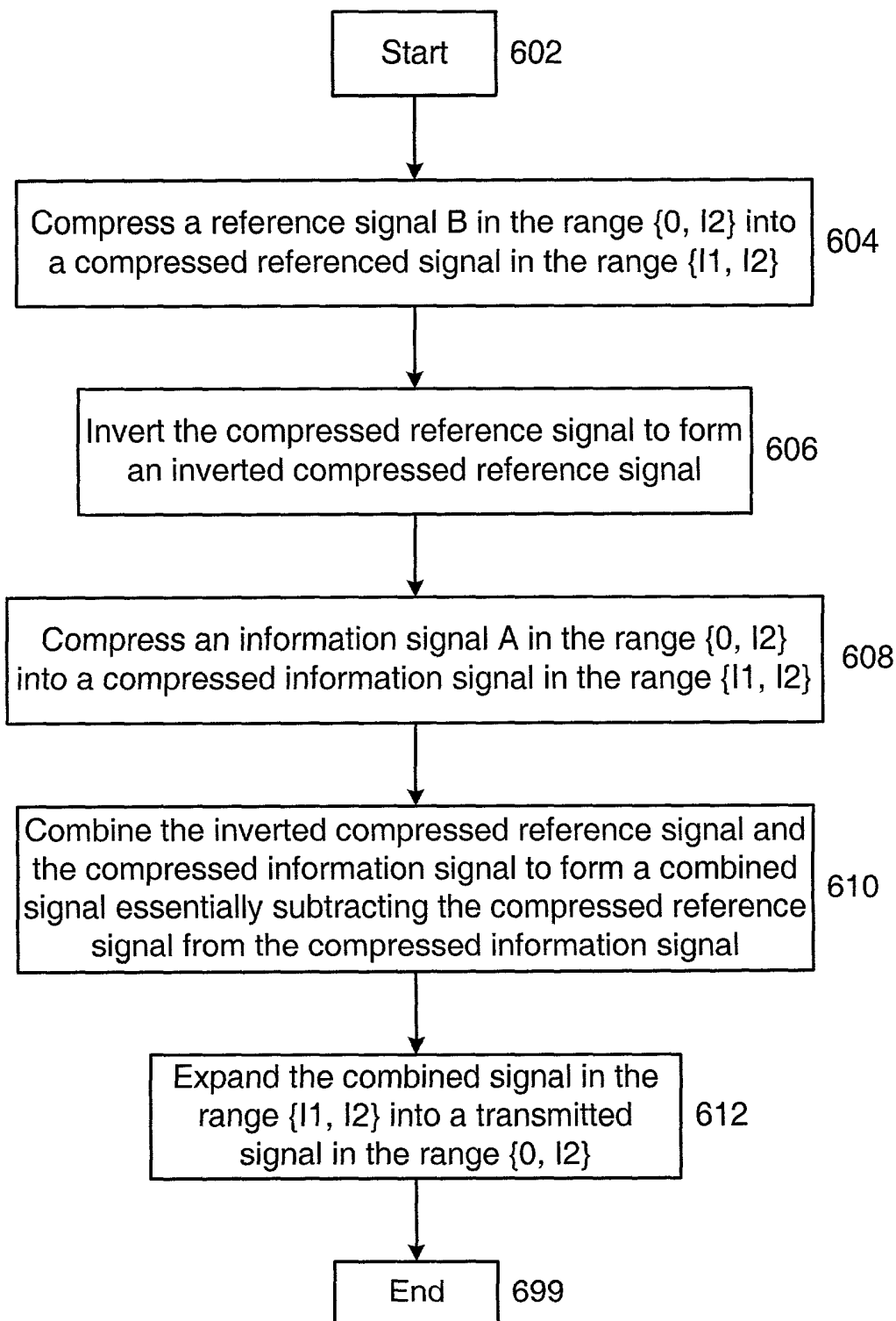


FIG. 16 1600

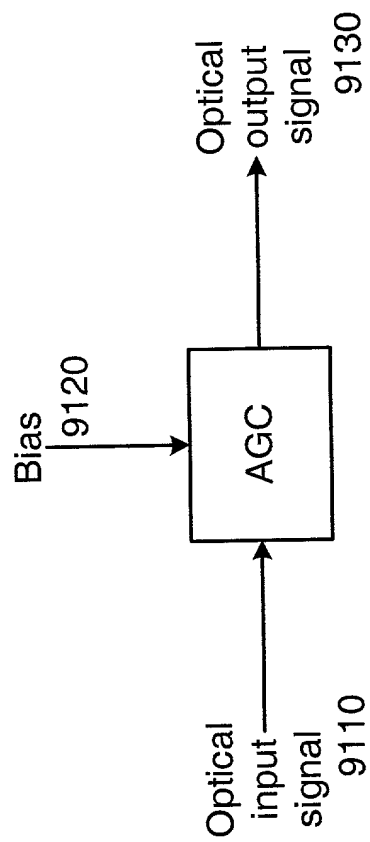


FIG. 17 9100

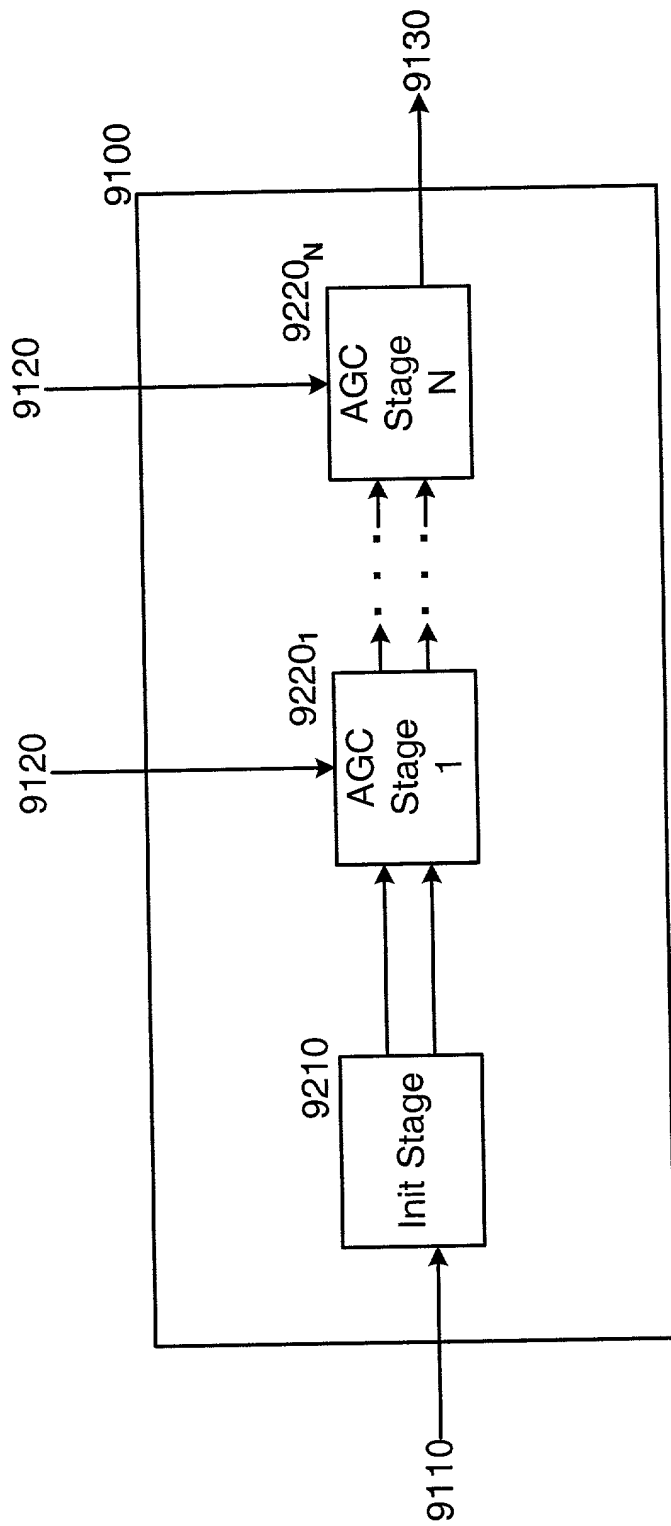


FIG. 18

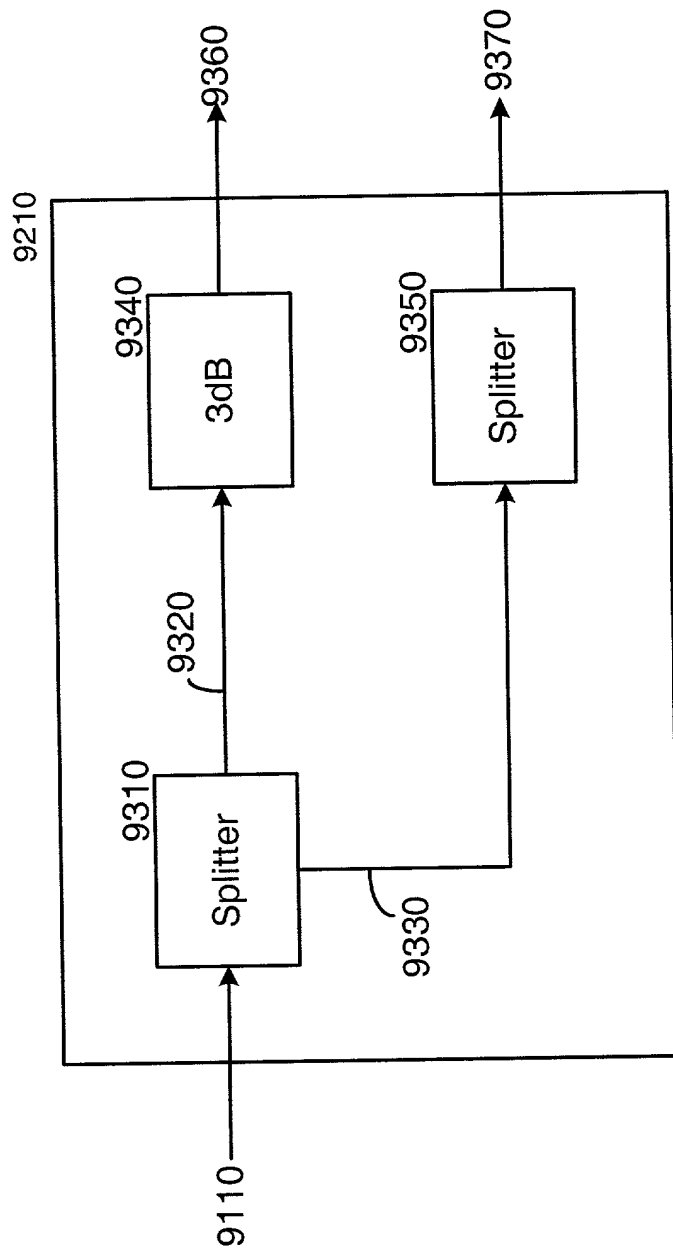


FIG. 19

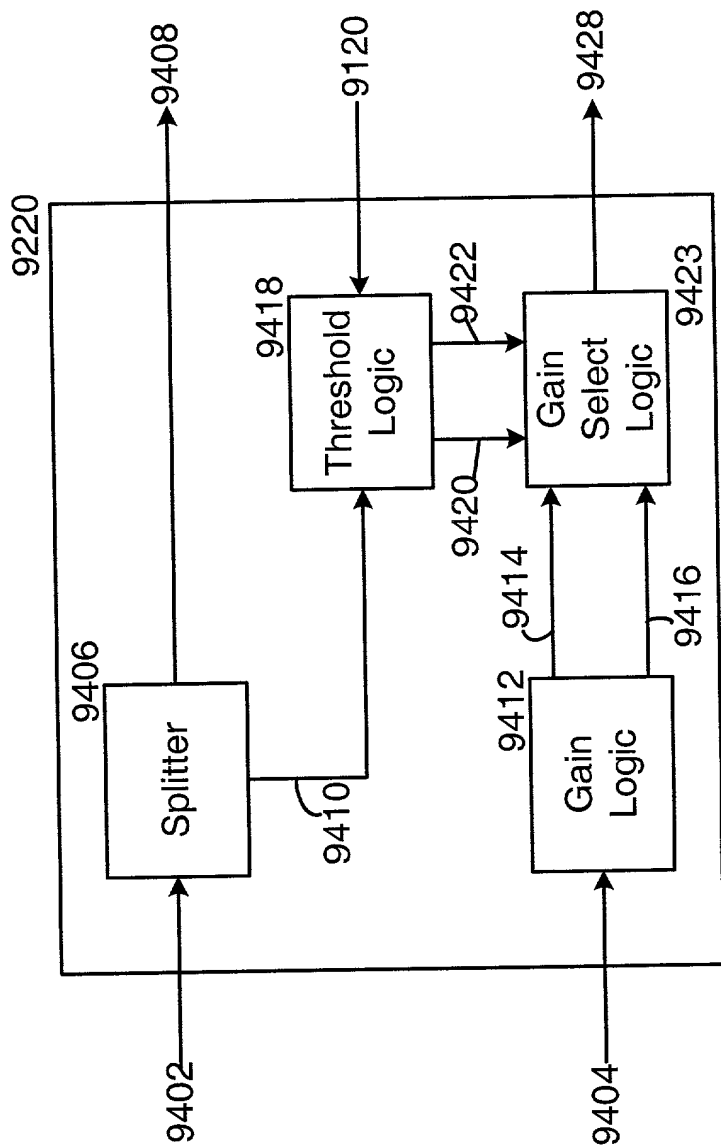


FIG. 20

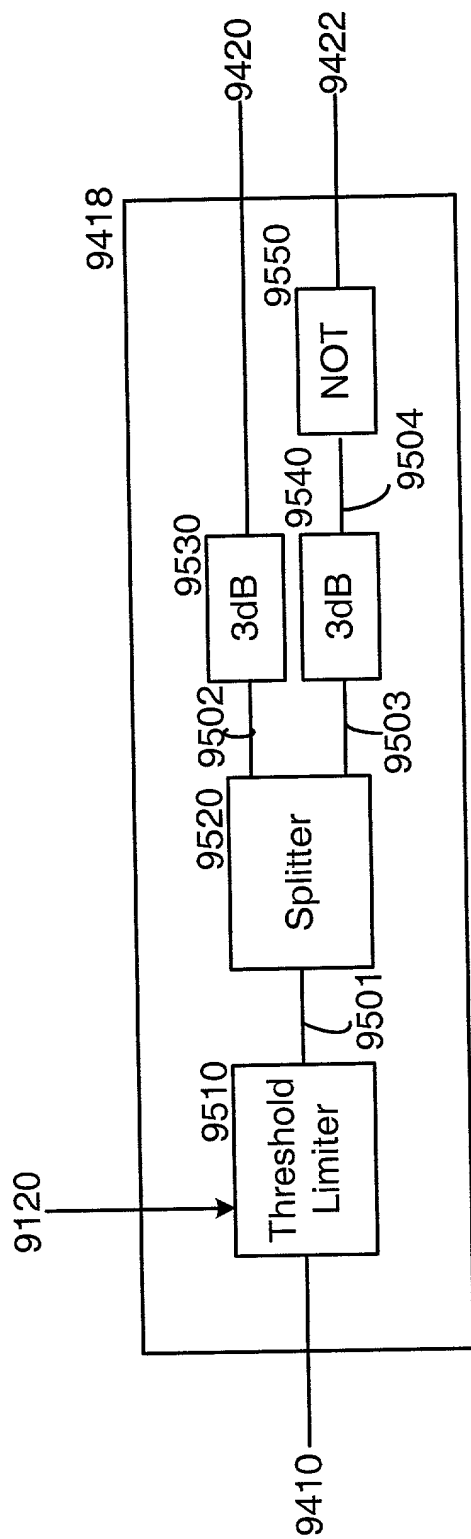


FIG. 21

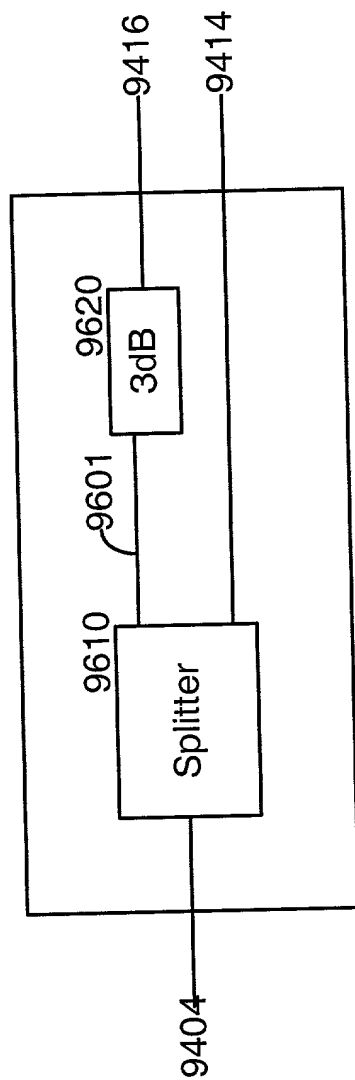


FIG. 22

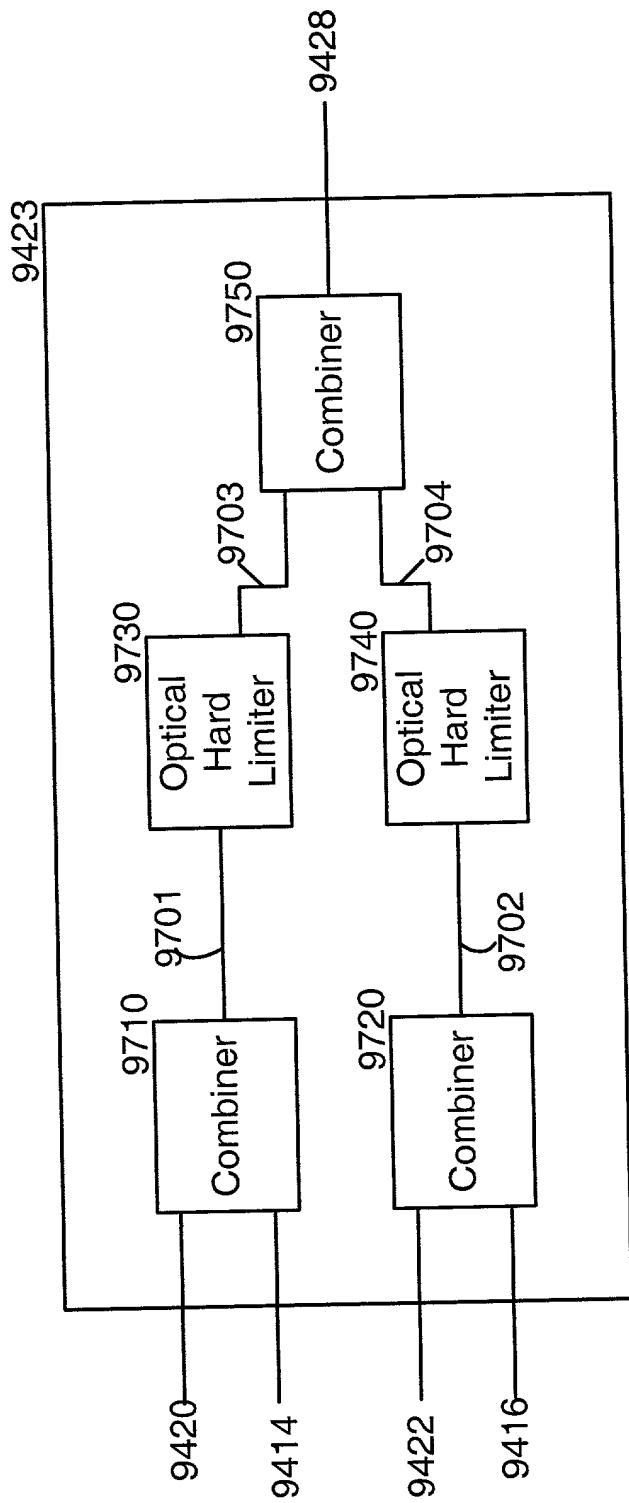


FIG. 23

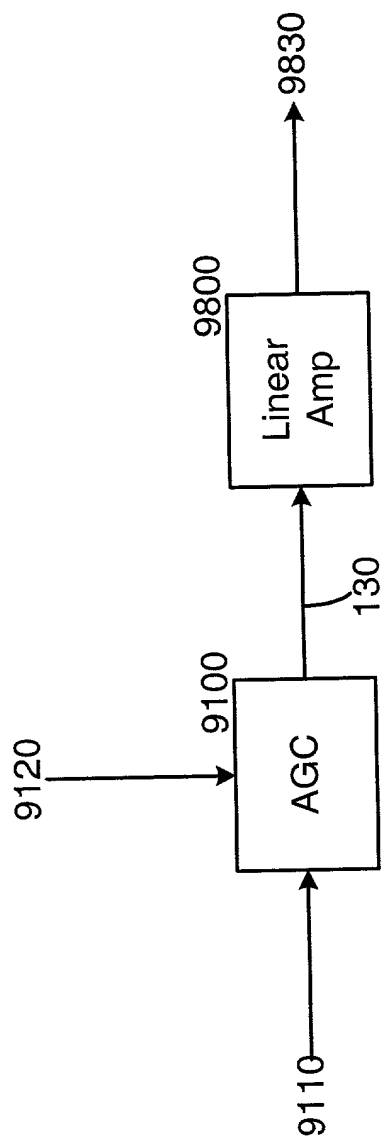


FIG. 24

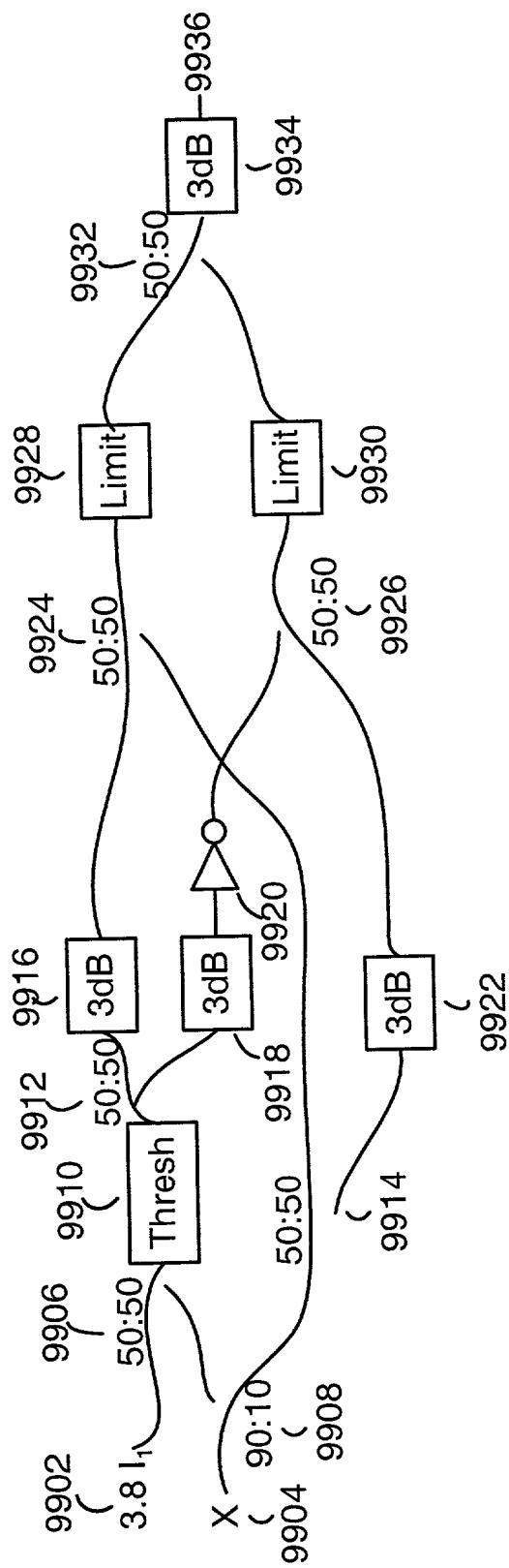


FIG. 25 9900

data bits							address bits					flag
D	D	D	D	D	D	D	N	N-1	• • •	2	1	F

FIG. 26 8100

20250724 10:00:00

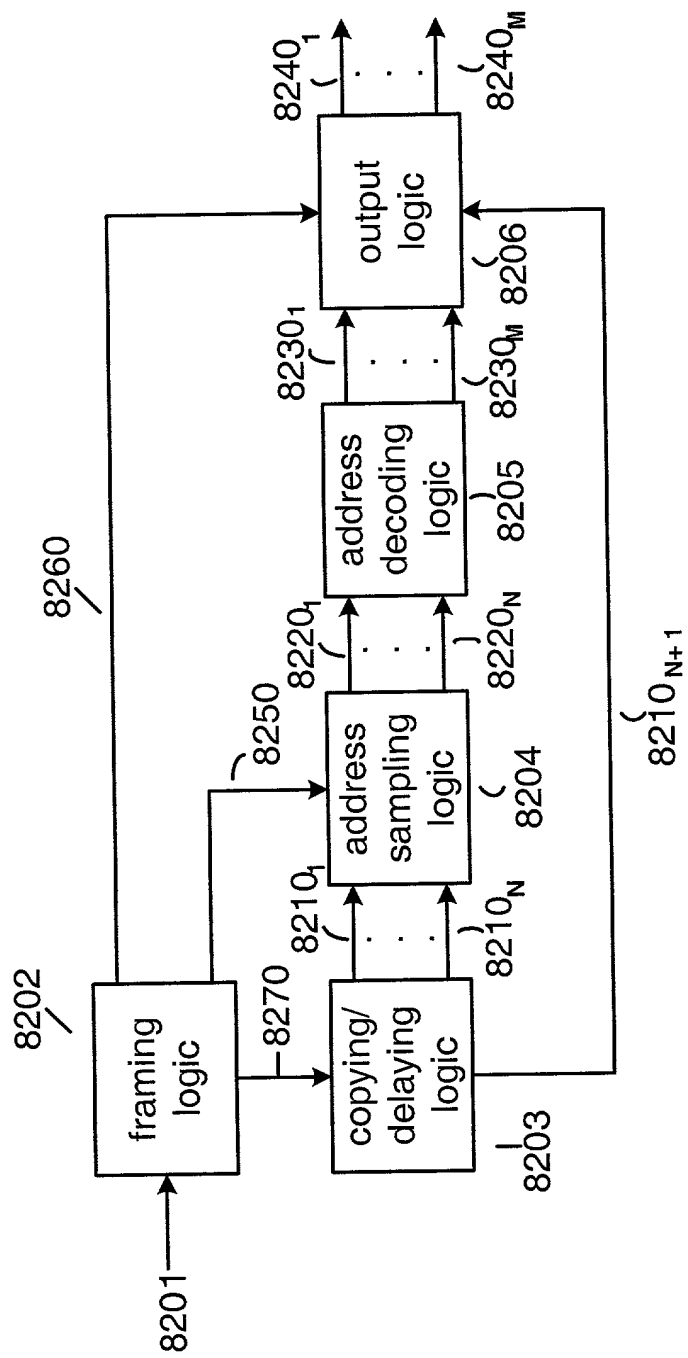


FIG. 27 8200

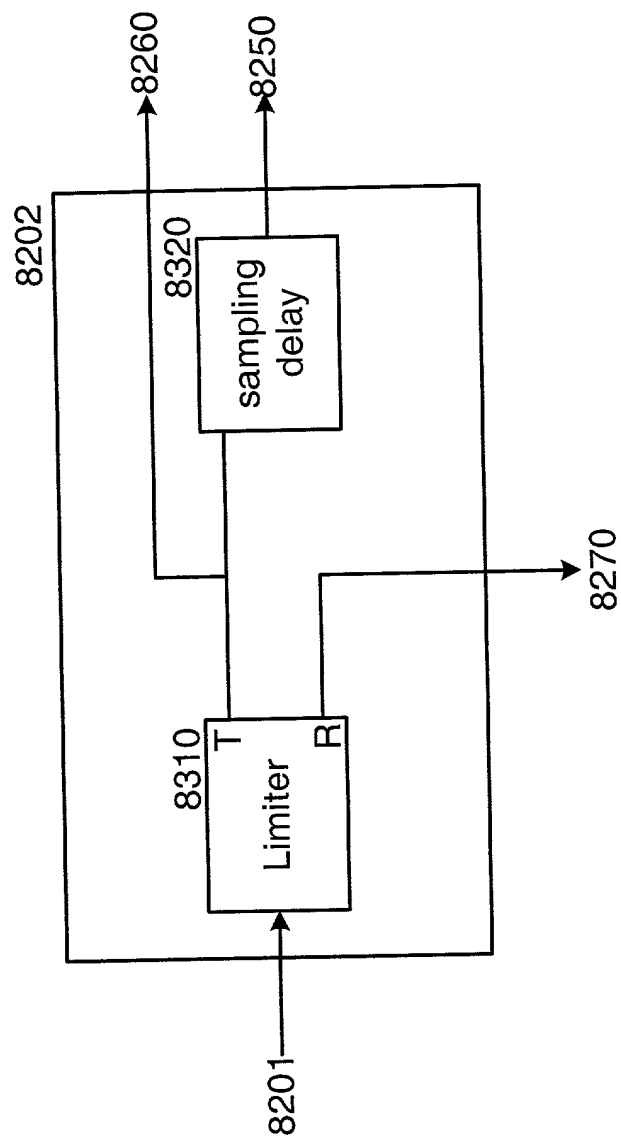
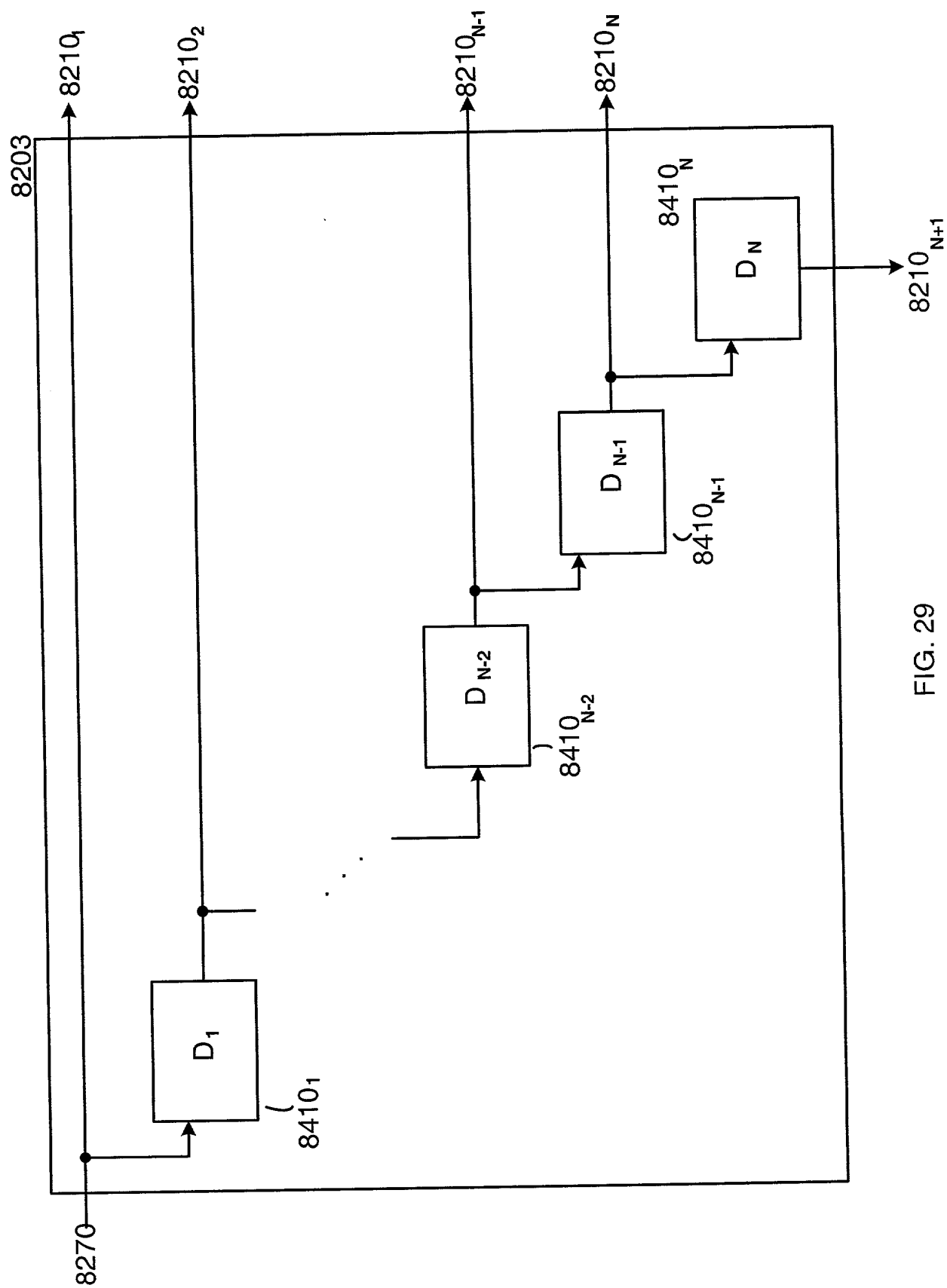


FIG. 28



▼														
							N	N-1		• • •		2	1	F
						N	N-1		• • •		2	1	F	
•														
•														
•														
		N	N-1		• • •		2	1	F					
	N	N-1		• • •		2	1	F						
N	N-1		• • •		2	1	F							

FIG. 30

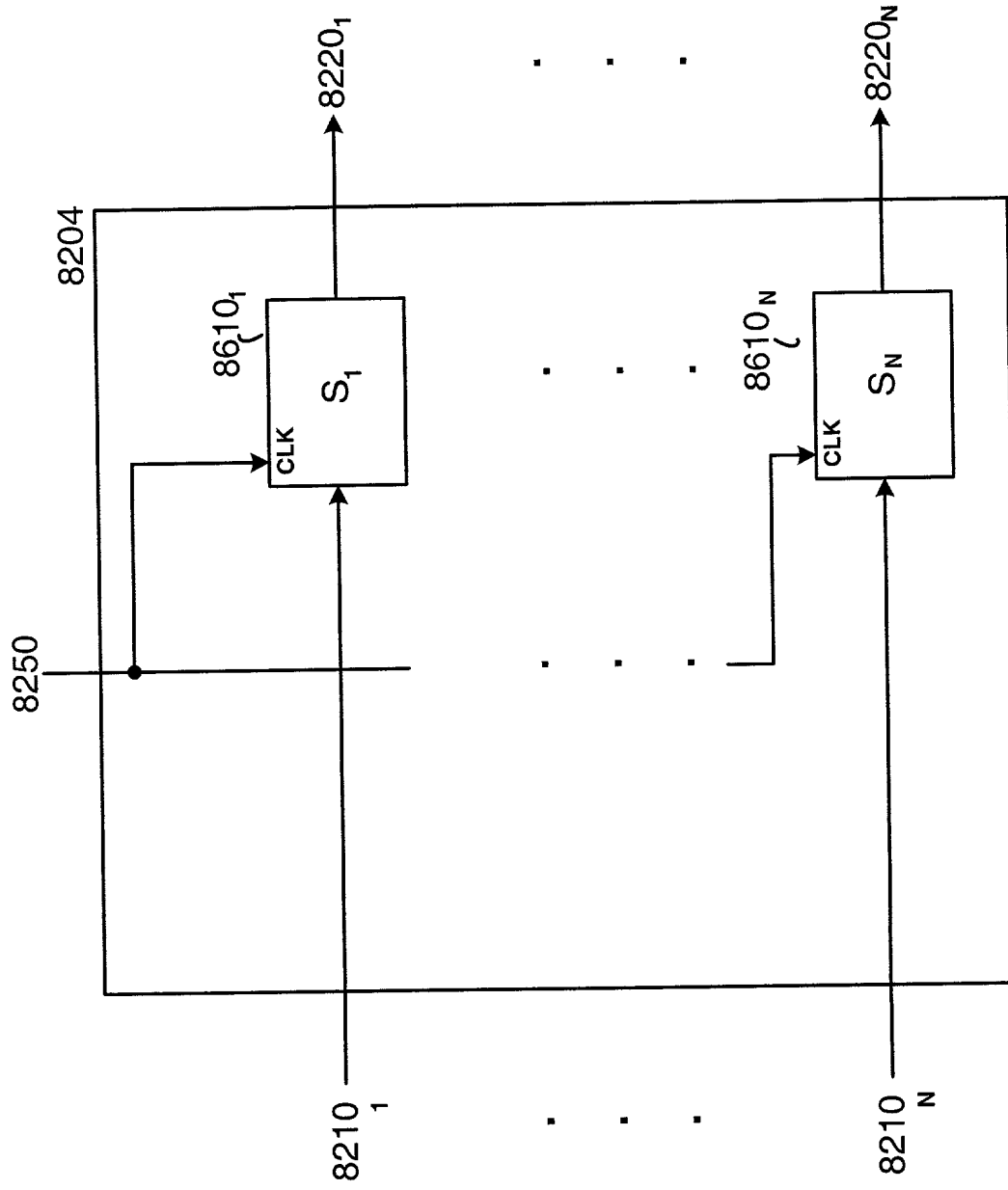


FIG. 31

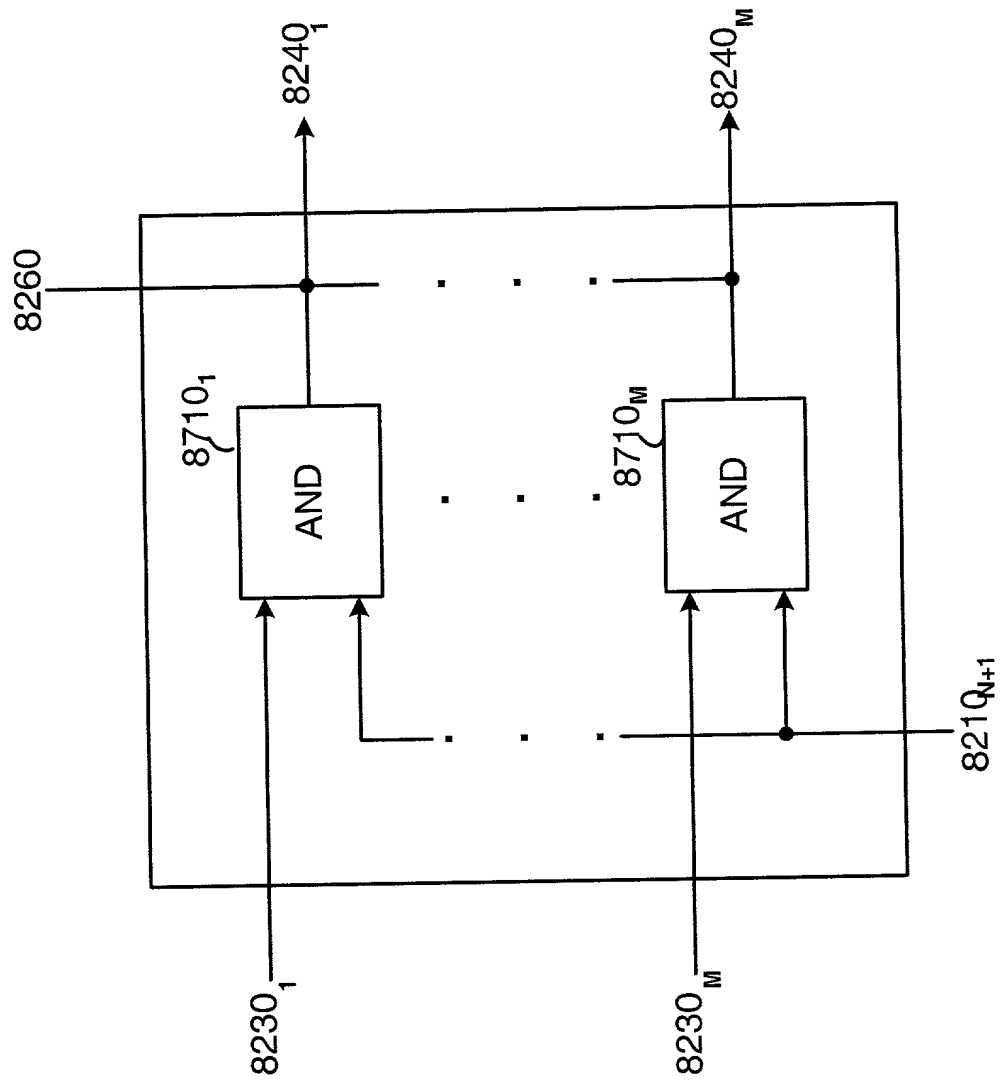


FIG. 32

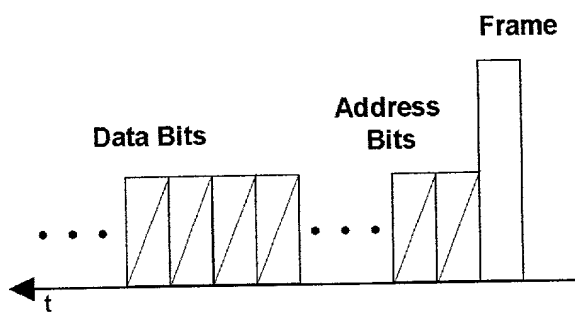


FIG. 33 8800

202020" 24429001

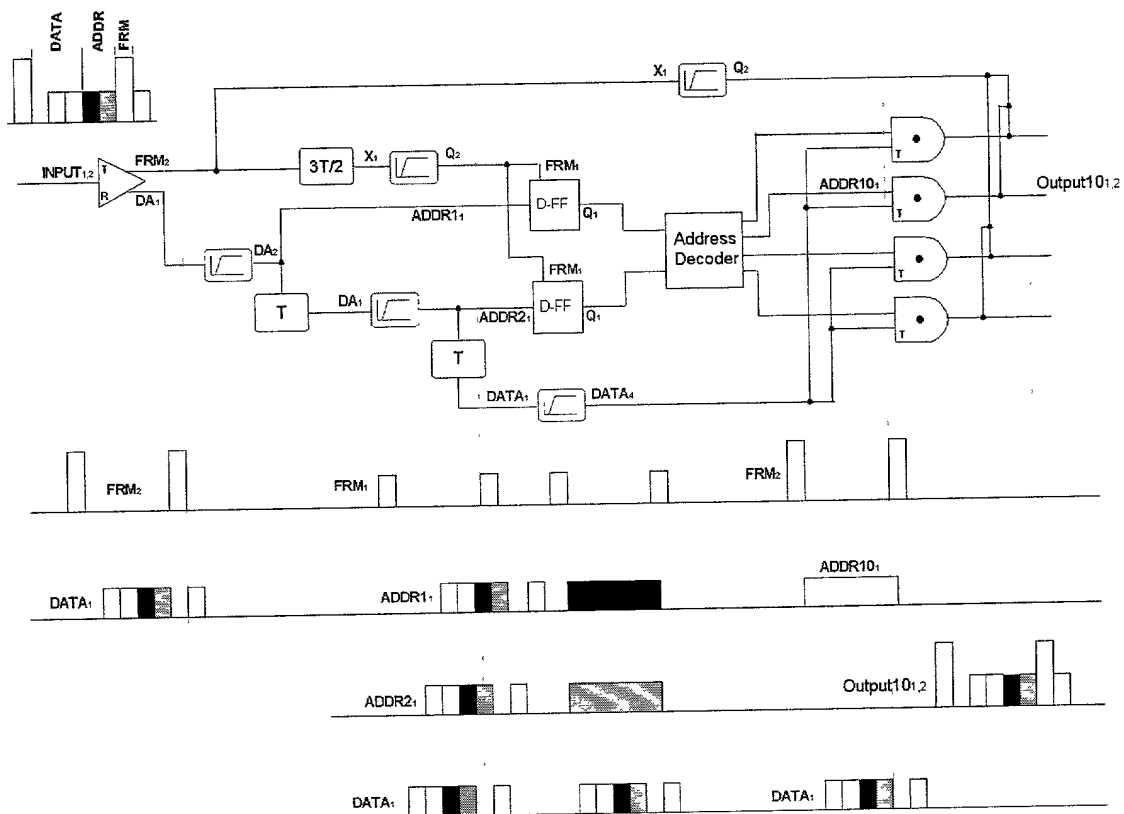


FIG. 34 8900

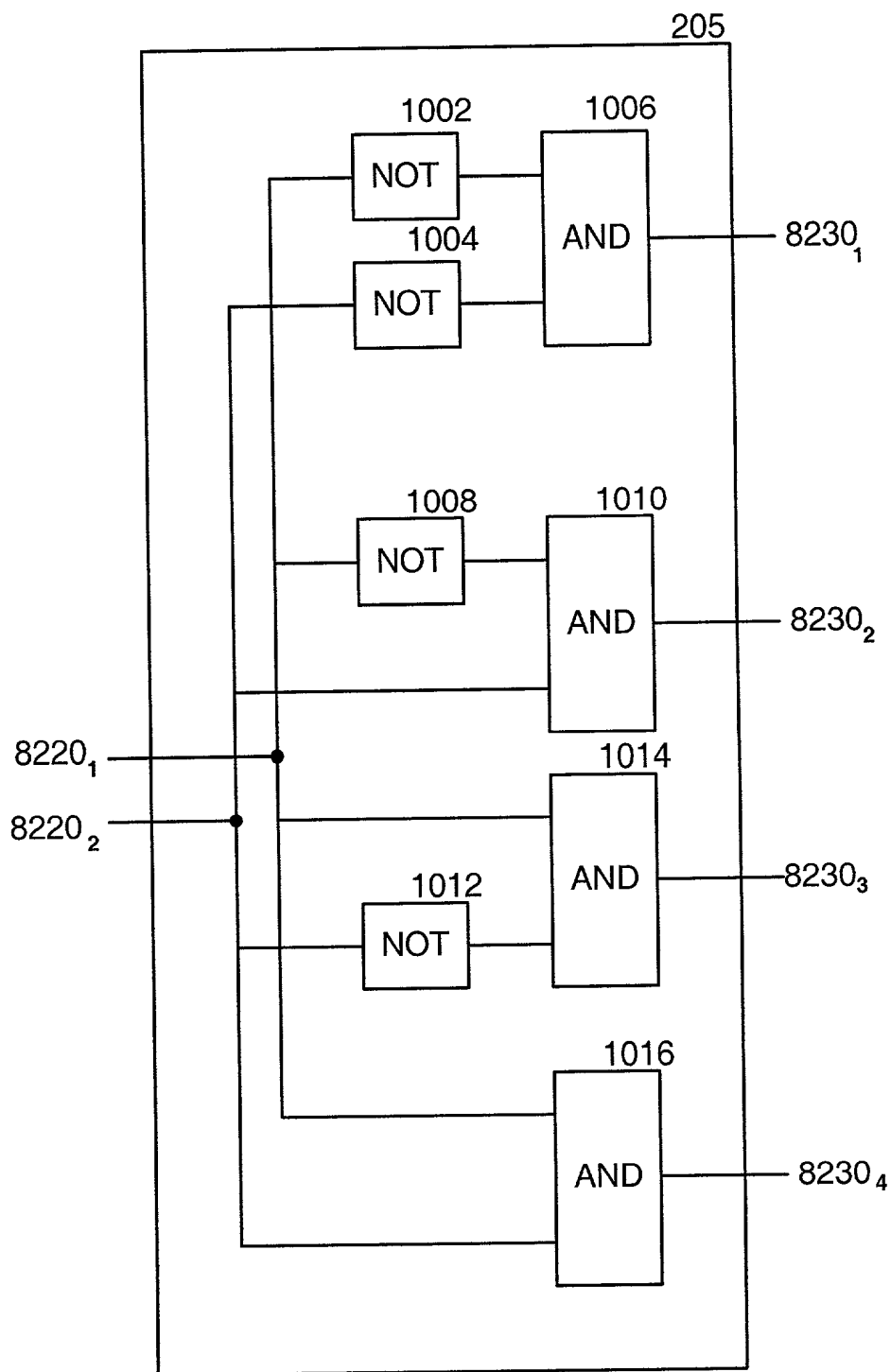


FIG. 35

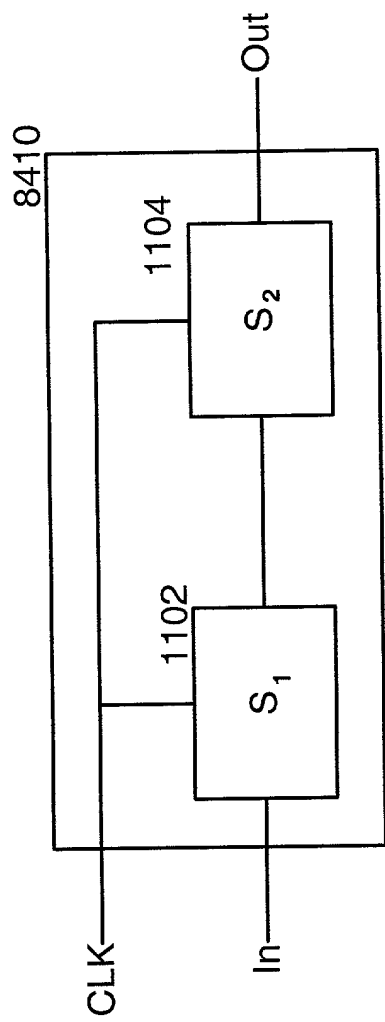


FIG. 36

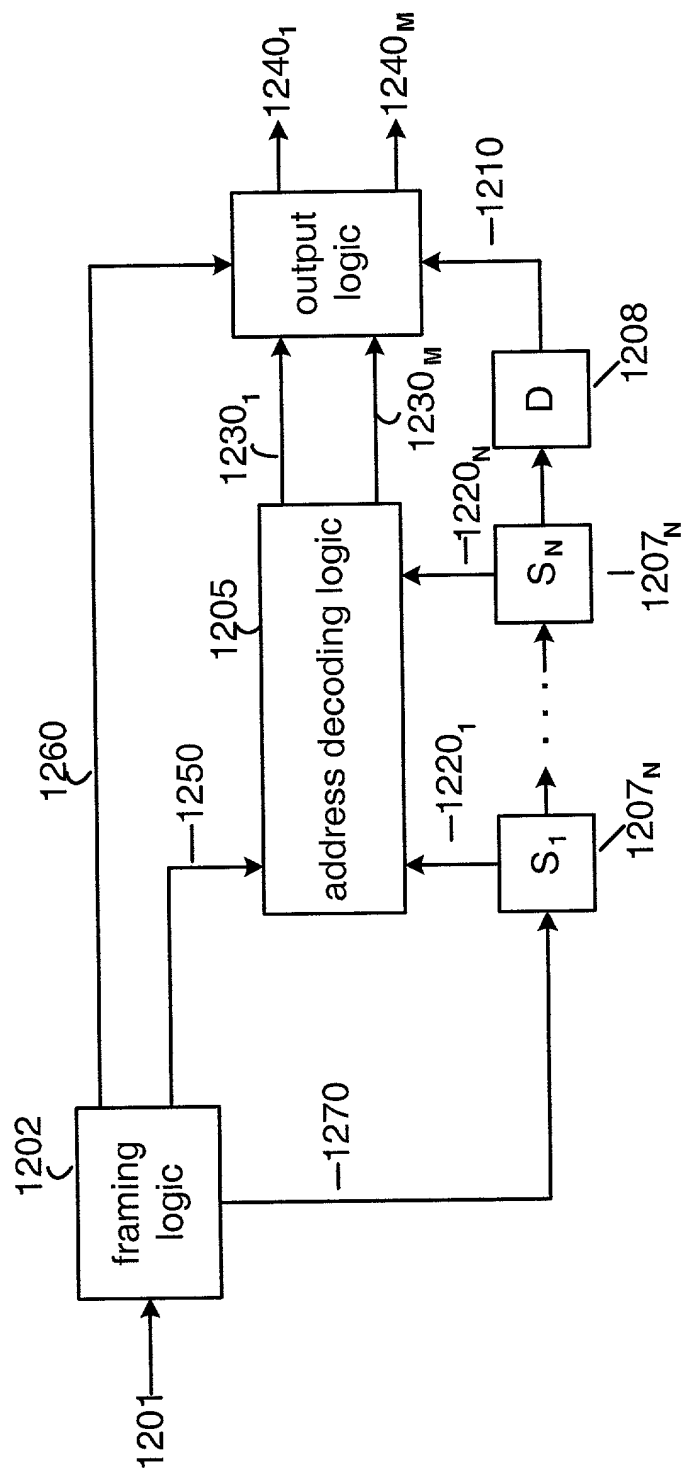


FIG. 37 3700

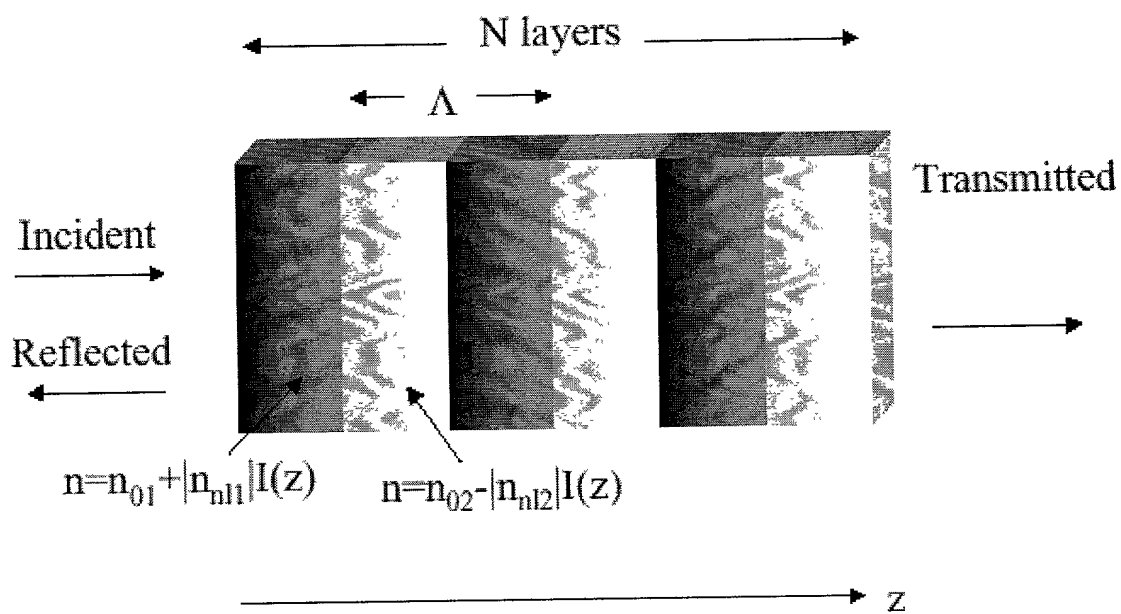


FIG. 38

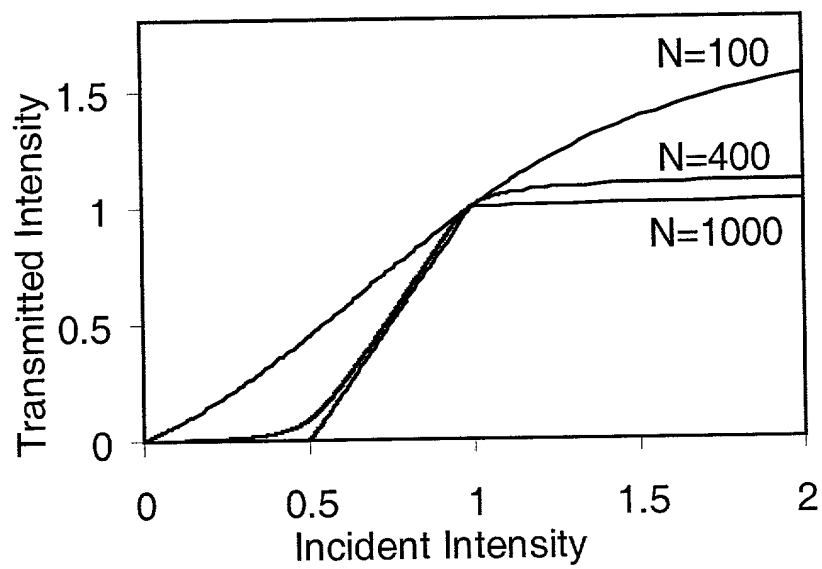


FIG. 39

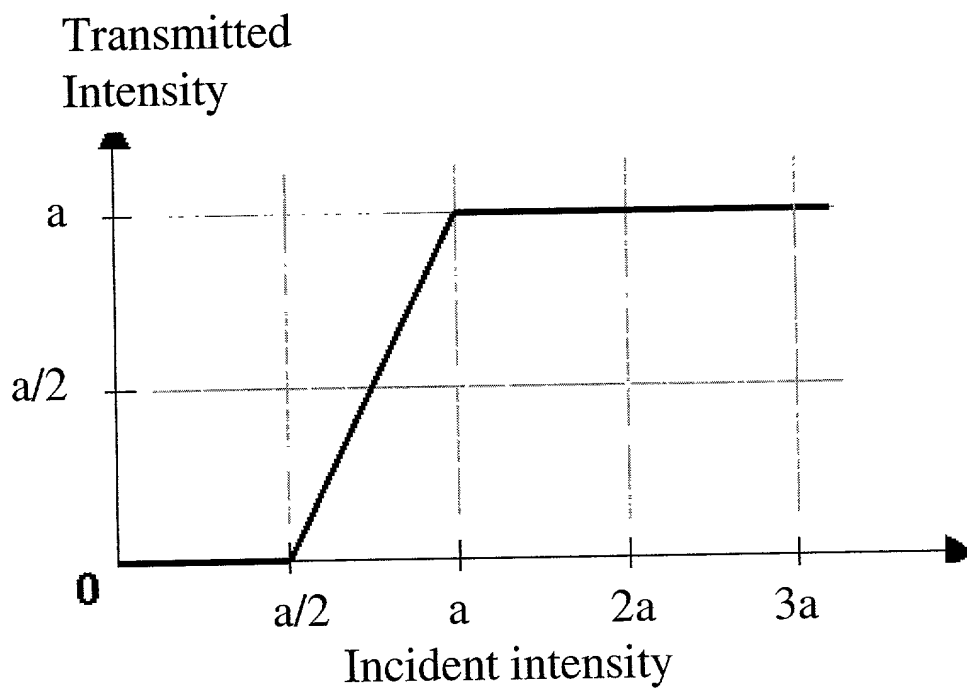


FIG. 40

20200220 22:48:00 1006047.03000

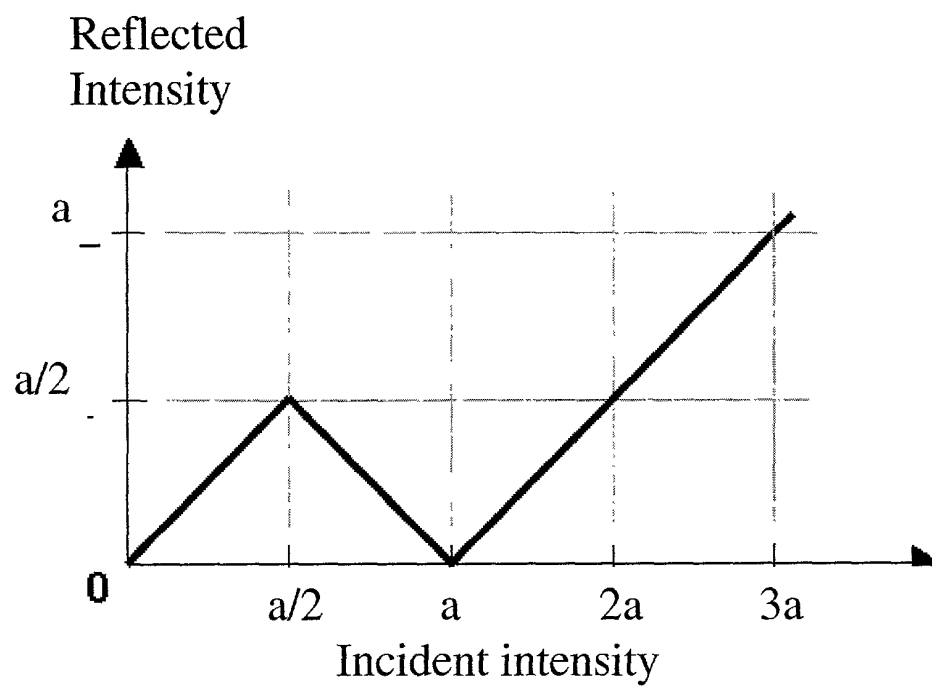


FIG. 41

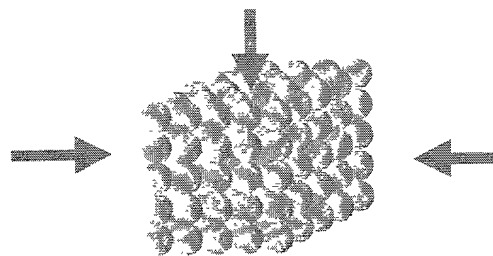


FIG. 42



FIG. 43

20200227183001

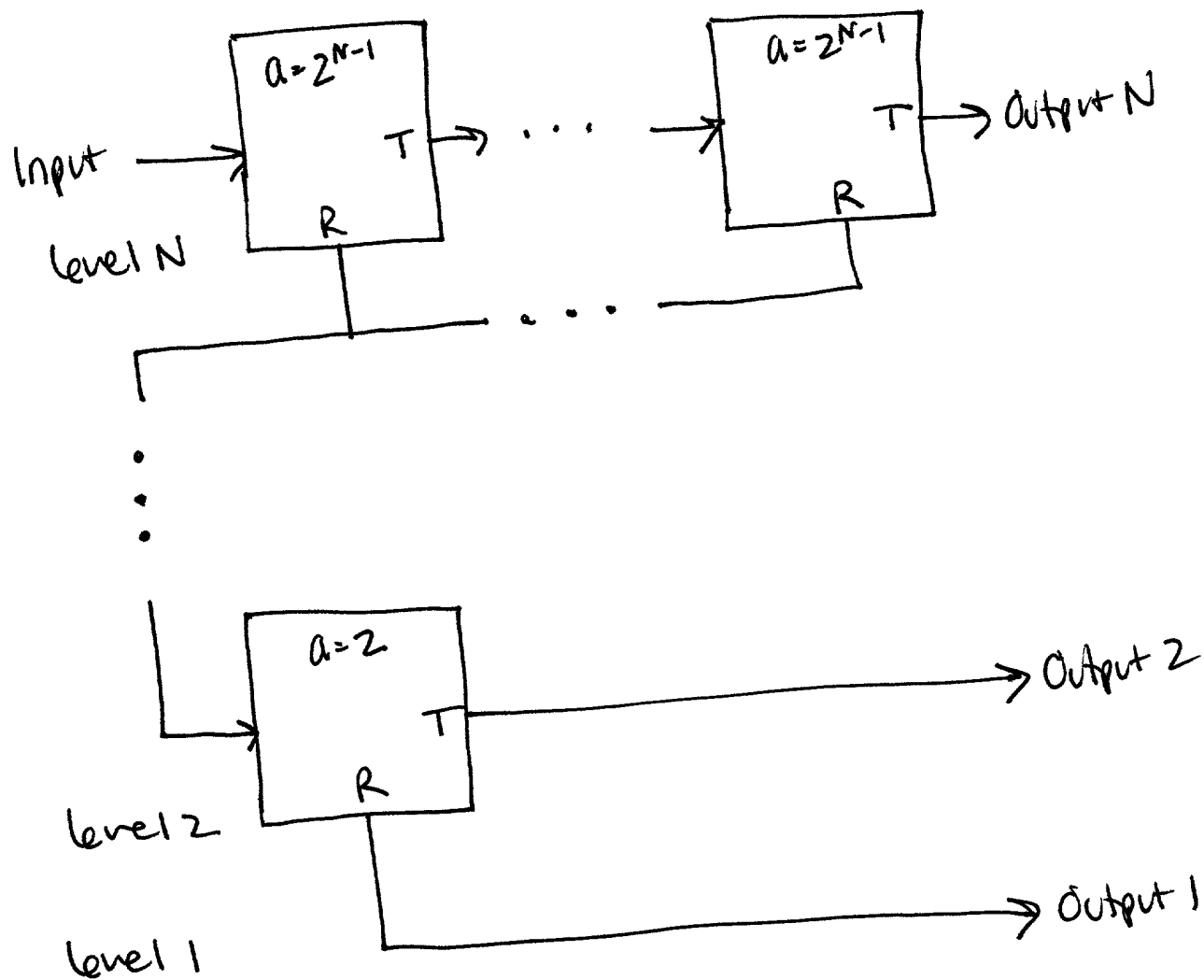


FIG. 44 4400

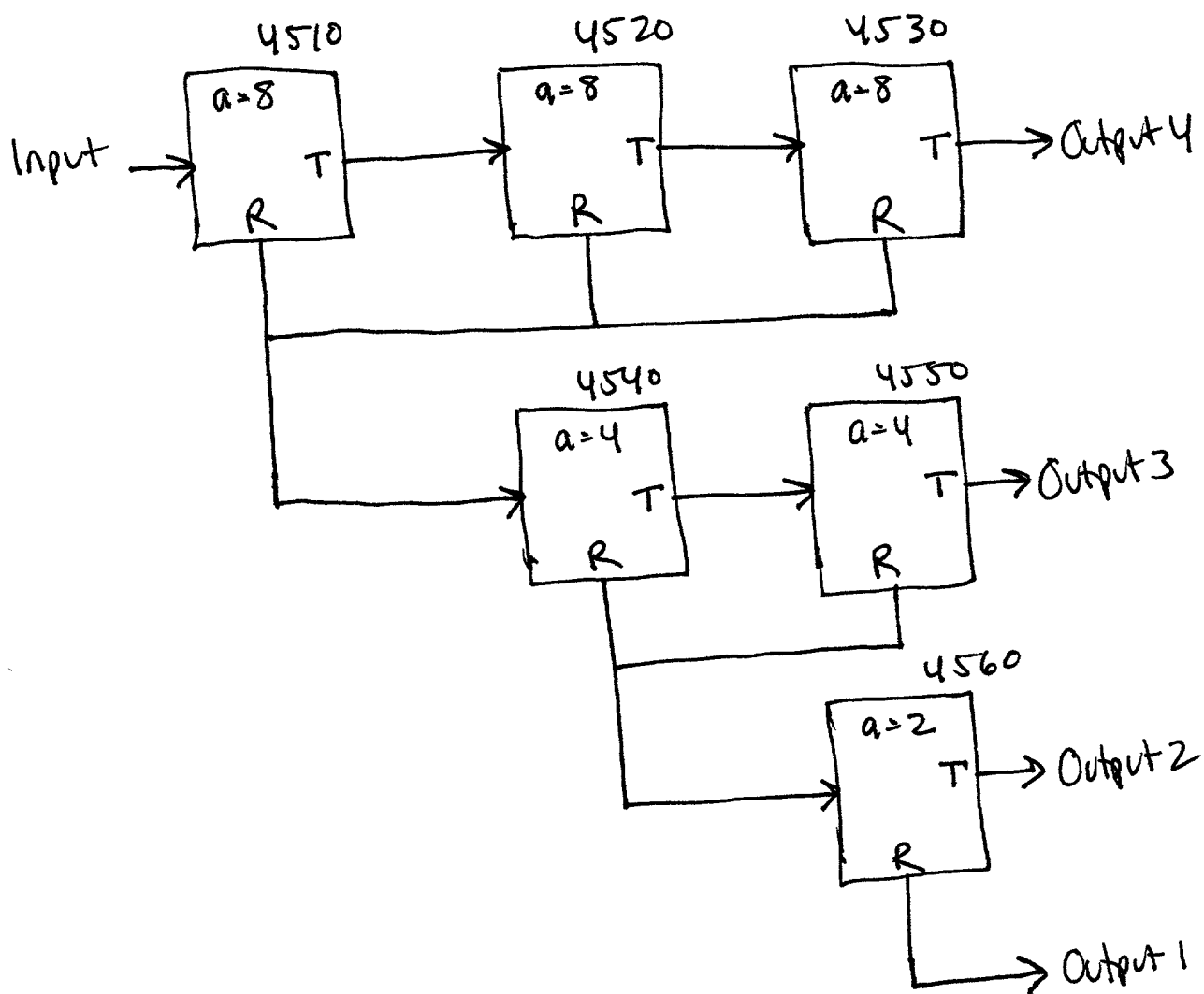


FIG. 45 4500

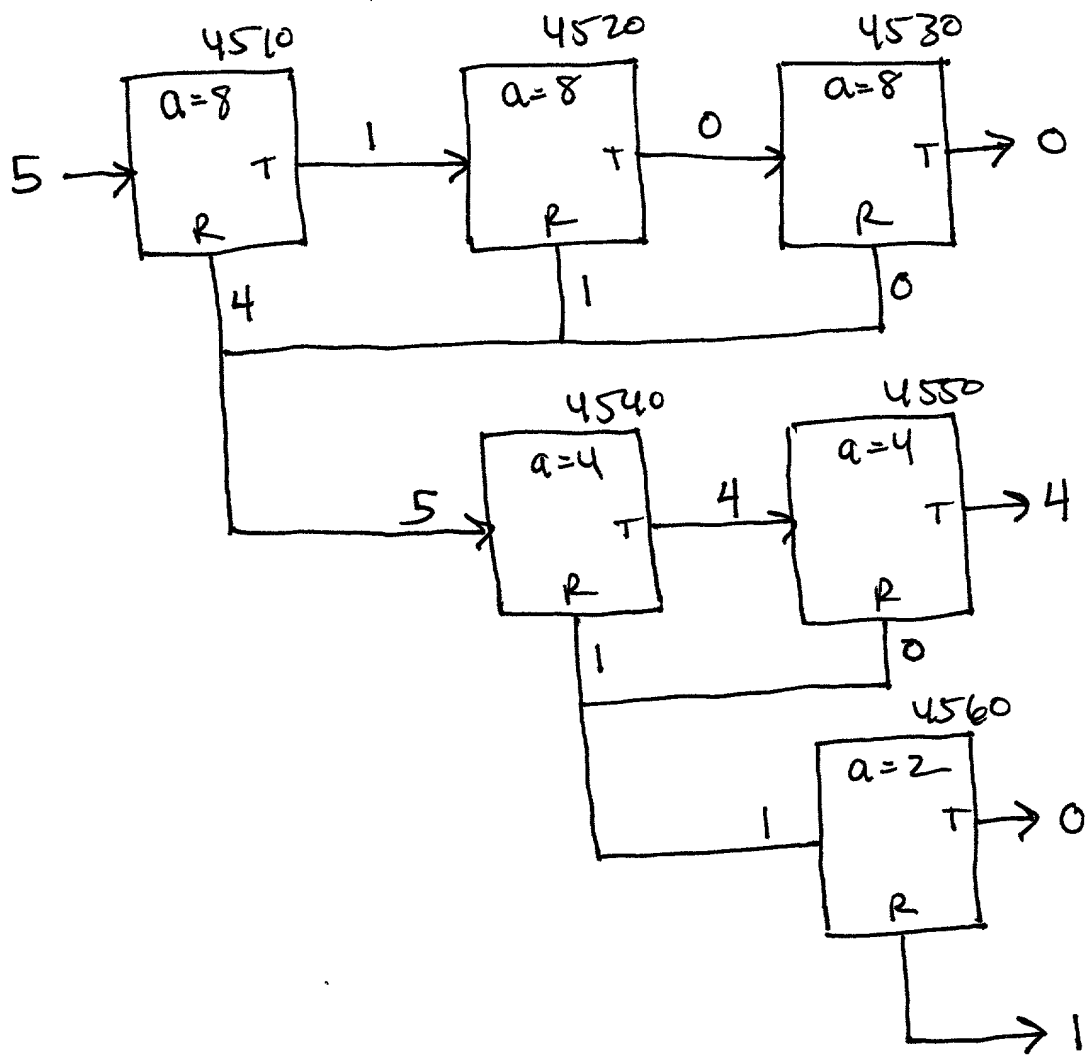


FIG. 46